



Inventor(s):

Nathan Moyle, Eric Mitchell and Mark Gehring

Serial No.:

10/796,873

Examiner:

Not yet known

Filing Date:

March 8, 2004

Group Art Unit:

2817

Title:

PHASE LOCKED LOOP OPERABLE OVER A WIDE FREQUENCY RANGE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

under 37 CFR 1.97 (b); or

(Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; whichever occurs last)

Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Elmer W. Galbi, Reg. No. 19,761

One La

MARGER JOHNSON & McCOLLOM, P.C. 1030 SW Morrison Street Portland, OR 97205 (503) 222-3613

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date: January 🦓 200

Beth A. Nichols

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	U.S. PATENT DOCUMENTS					
	Exam <u>Init Ref</u>	Document Number	Issue <u>Date</u> <u>Nam</u>	<u>e</u> <u>C</u>	Sub Class Class	
		None				
	FOREIGN PATENT DOCUMENTS					
	Exam <u>Init Ref</u>	Document Number None	Publication <u>Date</u> <u>Cou</u>	ntry <u>1</u>	<u>Name</u>	
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	OTHER DOCUMENTS					
	Exam <u>Init</u> <u>Ref</u>	Author, Title,	Author, Title, Date, Pertinent Pages, Etc.			
	Roland Best, <i>Phase-Locked Loops</i> , ISBN: 0071412018, July 2003					

Examiner:

Date Considered: